

FIG. 1

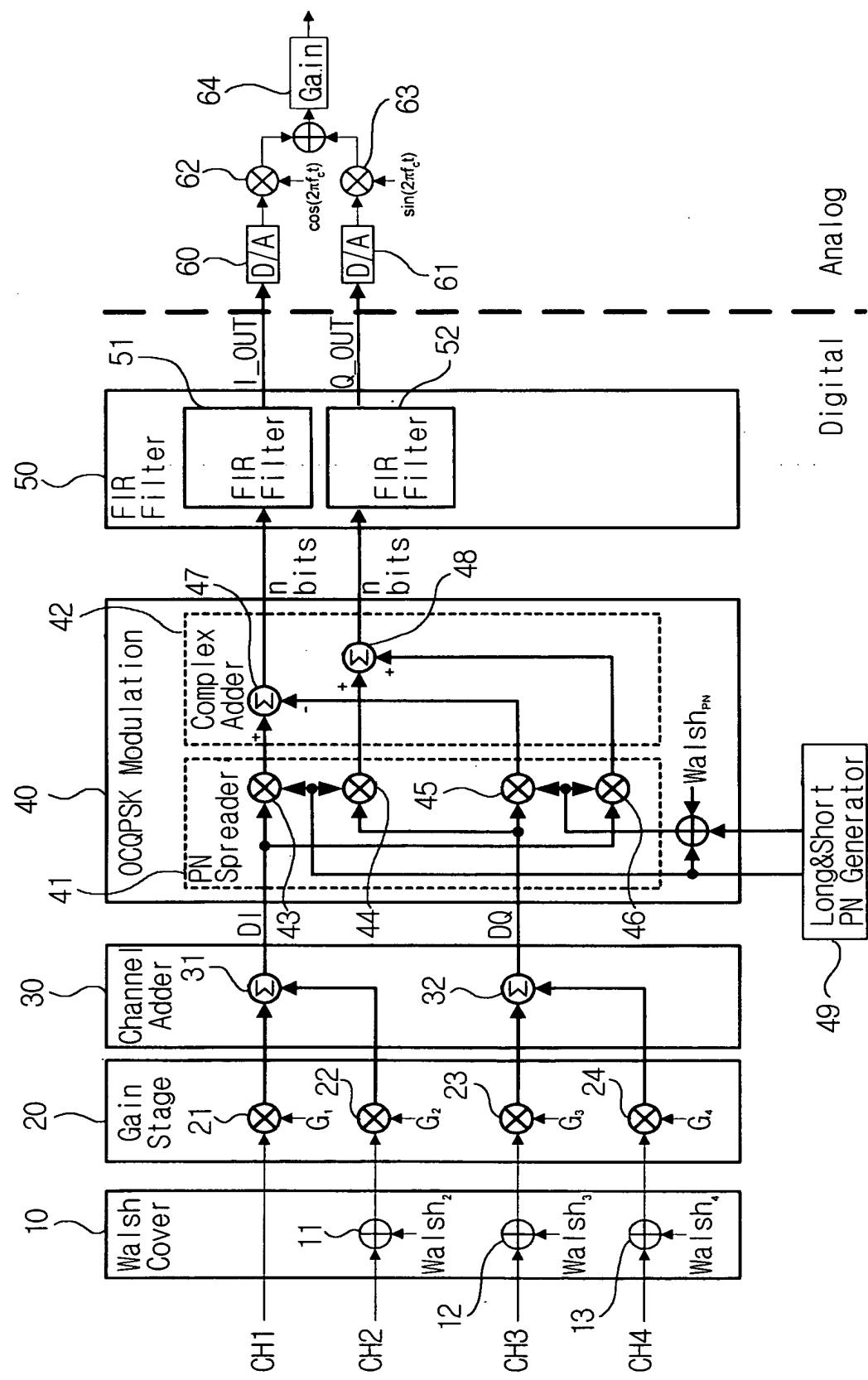


FIG. 2

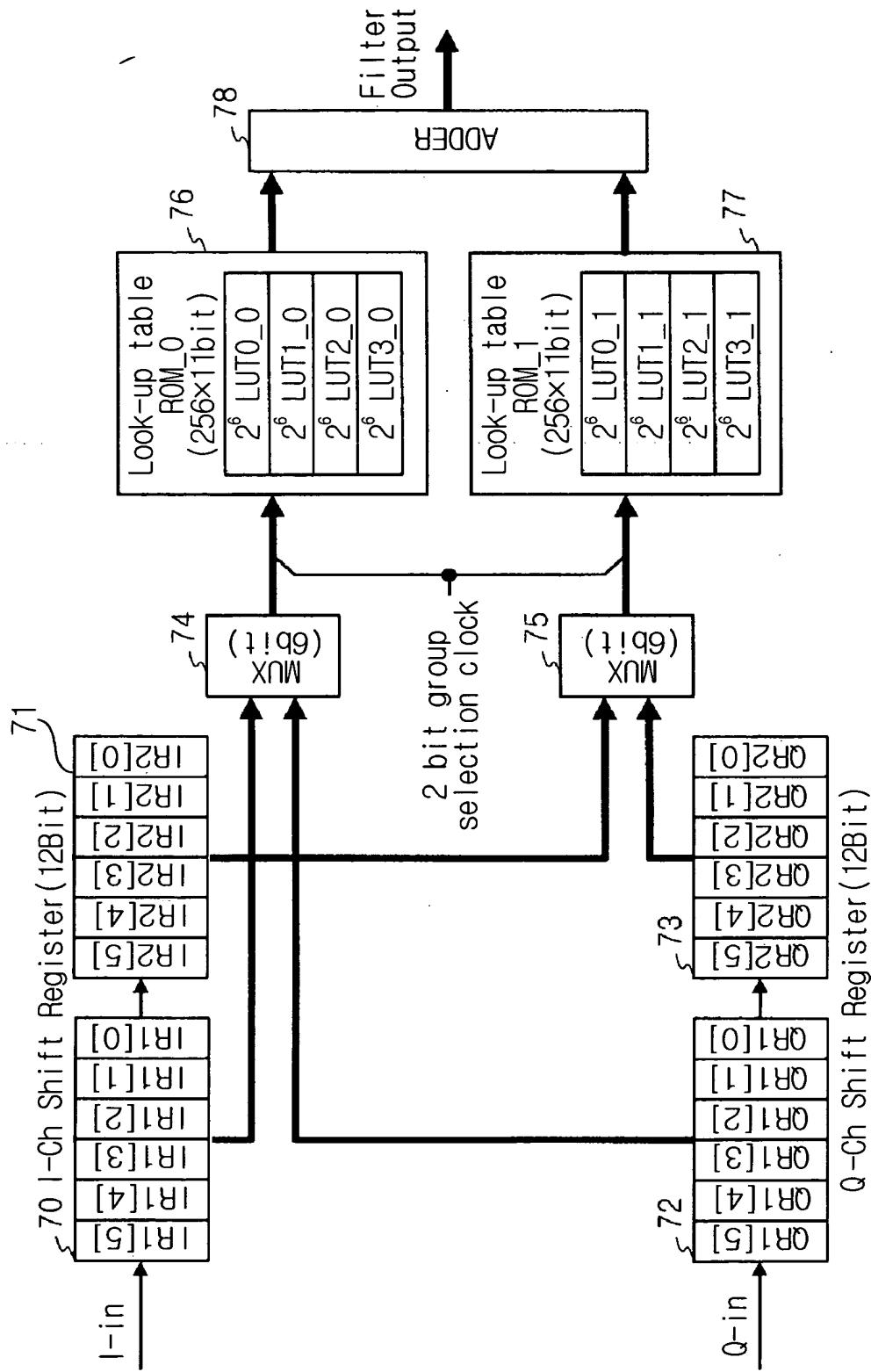


FIG. 3

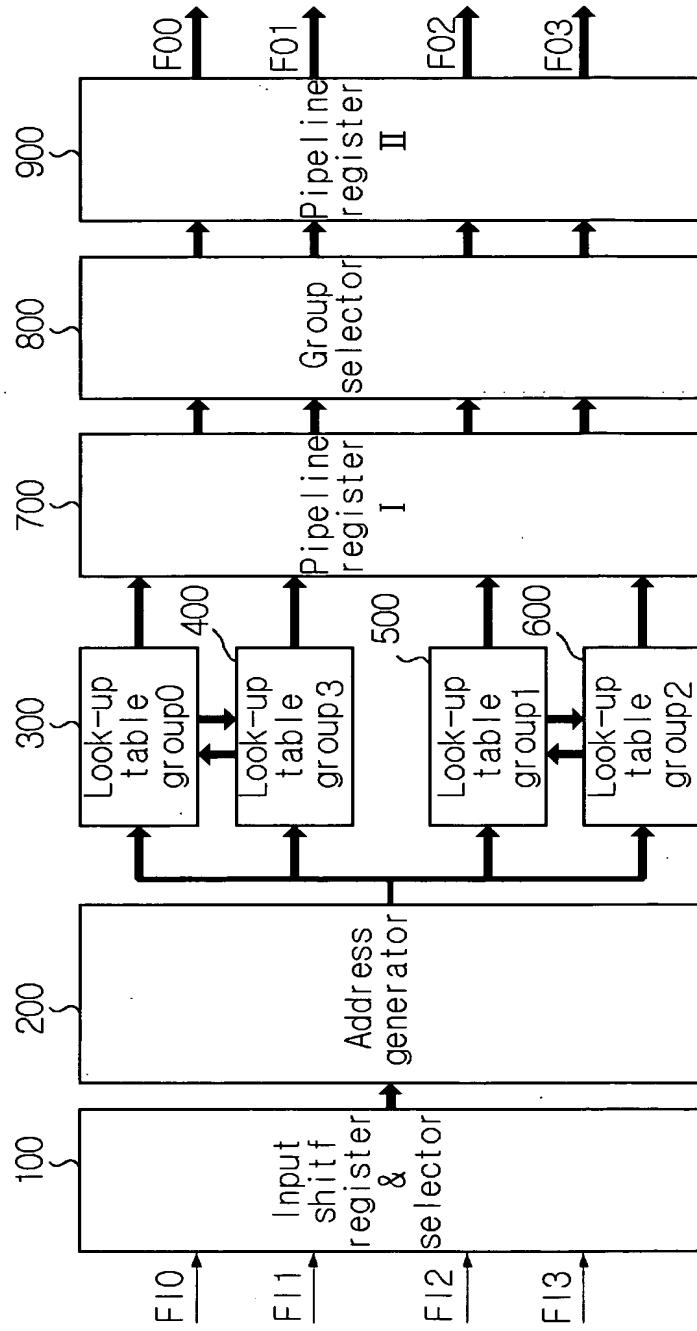


FIG. 4

Total	108 Tap	0 1 ... 22 23 24 25 ... 46 47 48 ... 59 60 61 ... 82 83 84 85 ... 106 107
Look-up table group 0	LUT_0	LUT_1 LUT_C LUT_2 LUT_3
Look-up table group 1	2^5 LUT0_0	2^5 LUT0_1 2^5 LUT0_C 2^5 LUT0_2 2^5 LUT0_3
Look-up table group 2	2^5 LUT1_0	2^5 LUT1_1 2^5 LUT1_C 2^5 LUT1_2 2^5 LUT1_3
Look-up table group 3	2^5 LUT2_0	2^5 LUT2_1 2^5 LUT2_C 2^5 LUT2_2 2^5 LUT2_3
Look-up table group 4	2^5 LUT3_0	2^5 LUT3_1 2^5 LUT3_C 2^5 LUT3_2 2^5 LUT3_3
Input shift register & selector output	A[26:21] A[20:15]	A[14:12] A[11:6] A[5:0]
	6 bit	6 bit 3 bit 6 bit 6 bit

0000000000000000

FIG. 5

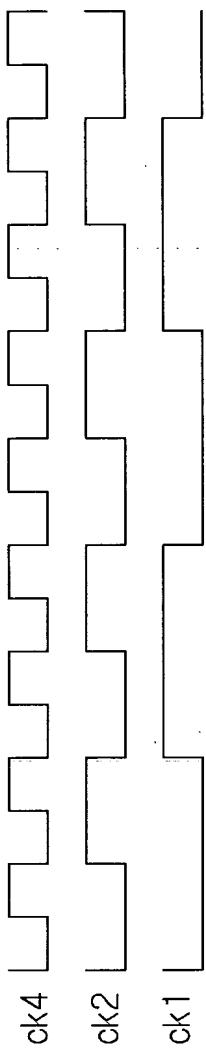


FIG. 6

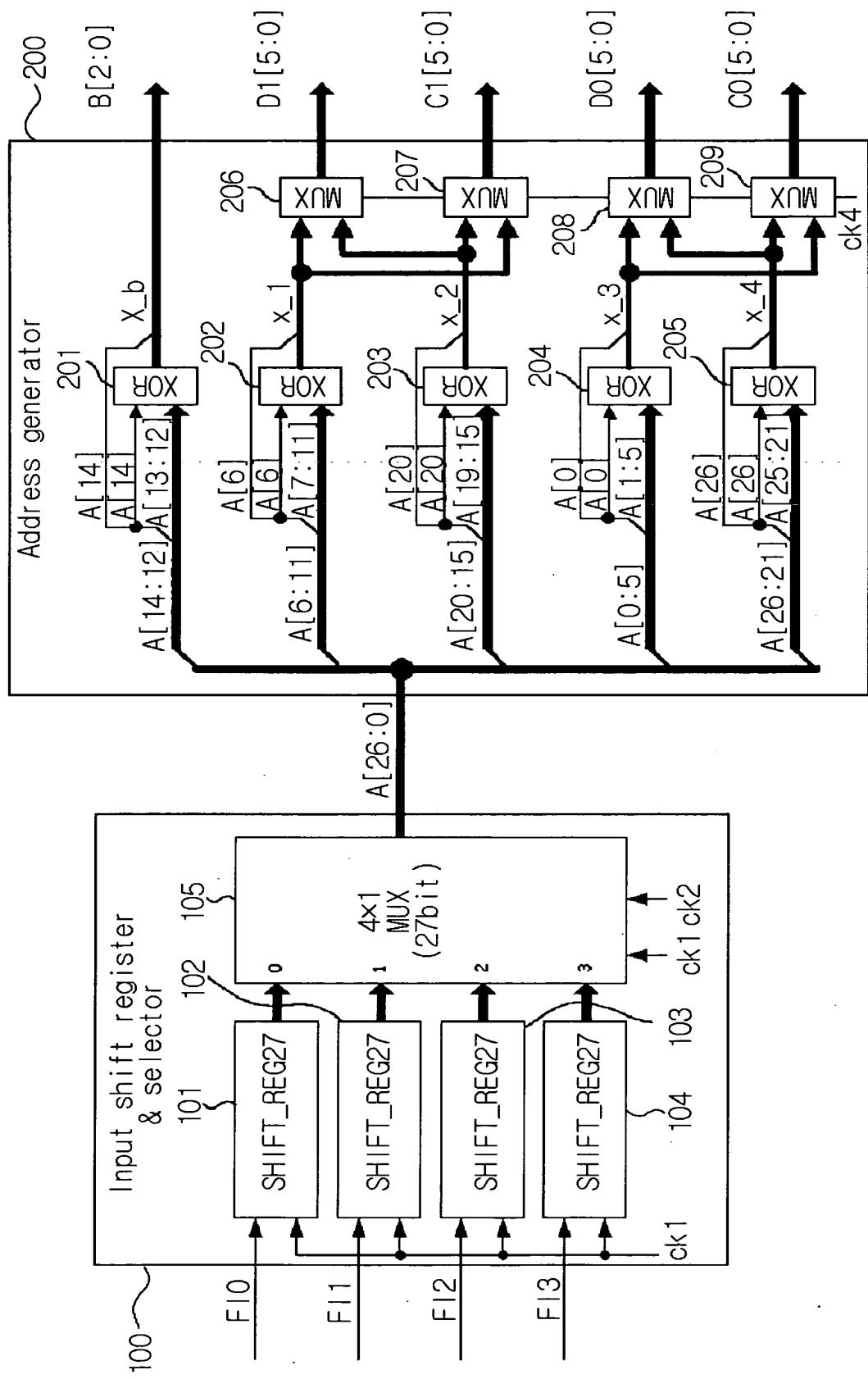


FIG. 7

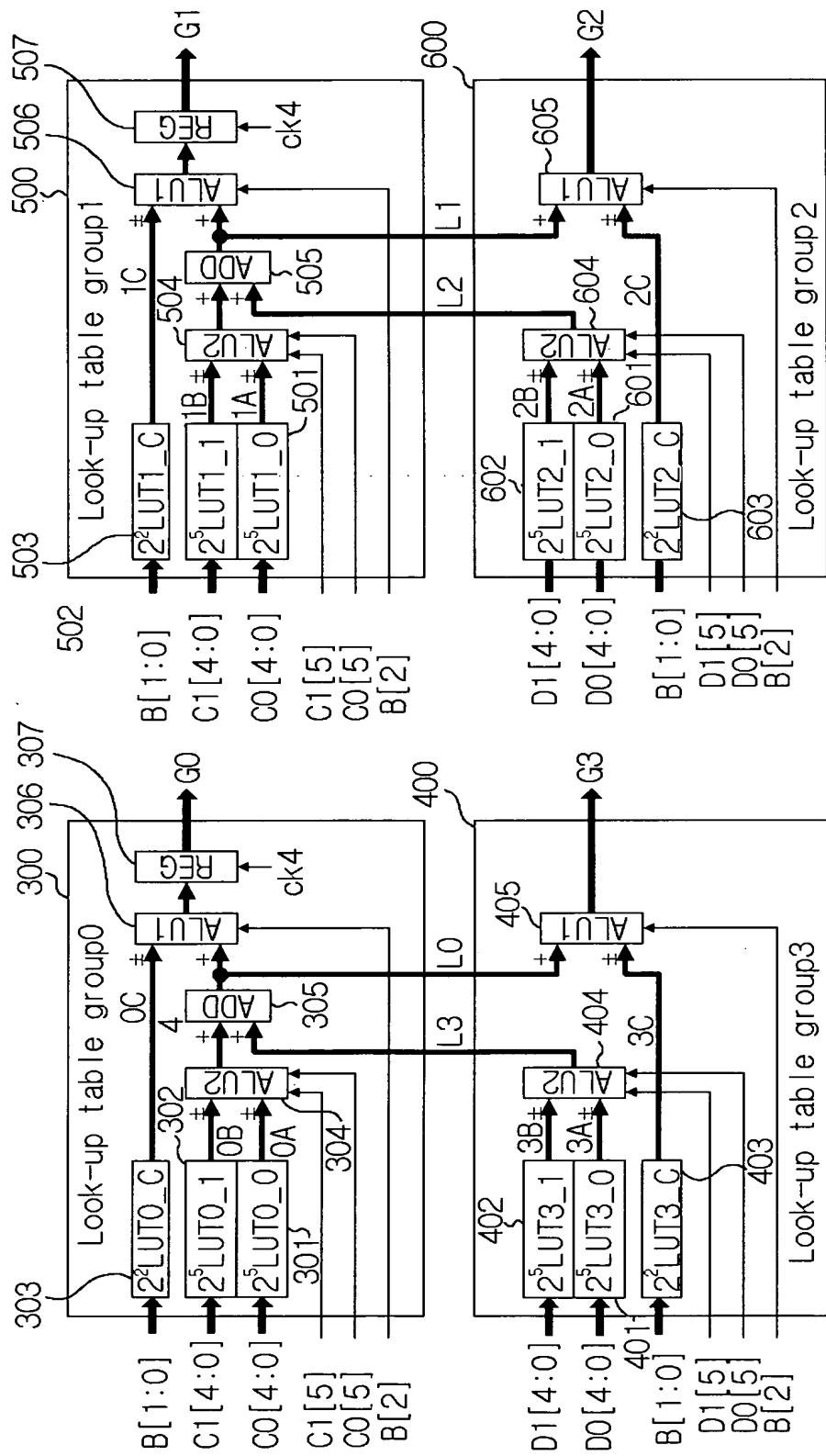


FIG. 8

